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11 December 2023
Version 1.12

Title word cross-reference

(d, k) [Ake65c, Fri66e, Kor67c]. (d, k, μ) [Qua69]. 1 [Wei69b]. 15 [Sei64]. 2 [EKP67, Fia66, Fre67a, Roy68b, SA64, Sch69b, Wei69b]. 21/2 [Gil66d]. 2×1 [Set66a]. 3 [AS66, Fre67a, Kei64, SA64, Wol69]. 4 [Fre67a]. 48 [Eps68]. $[n, k]$ [Lec64]. $_2$ [MS66]. $_3$ [MS66]. a [Mul59d, Mul60]. $\cos x$ [Spe65]. $\cot^{-1} x$ [Spe65]. $\exp x$ [Spe65]. F [Sed68, Hsi68]. γ [MS66]. k [Lem69, Liu63b, Liu64a]. $k = 4$ [Bra66]. $\ln x$ [Spe65]. m [Fro65]. N [Ber58, Ble61b, Dav65, Hig61b, MN61, BB62, Col69a, Fro65, Lee62a, Ull69b]. P^n [Mow66]. π [GH59]. r [HS69c]. $\Sigma(k)$ [Bra66]. $\sin x$ [Spe65]. $t*$ [Tay65]. $\tan^{-1} x$ [Spe65]. θ [Bro60b]. X [Ort59]. x^y [Kri64]. Z [Tou60, Fuk64].

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[Azg68] V. Azgapetian. Comment on "An Analog Photoresistive Multiplier". *IEEE Transactions on Computers*, C-17(2):188, February 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687312. See [Mas67].

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1967. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039043.

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[BCE61] D. R. Bennion, H. D. Crane, and D. C. Engelbart. A bibliographical sketch of all-magnetic logic schemes. *IRE Transactions on Electronic Computers*, EC-10 (2):203–206, June 1961. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219190.

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[Bek65] G. A. Bekey. R65-11 methods for solving engineering problems using analog computers. *IEEE Transactions on Electronic Computers*, EC-14(1):92–93, February 1965. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038371`.

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[Bek67] G. A. Bekey. R67-30 analog computer methods for parameter optimization. *IEEE Transactions on Electronic Computers*, EC-16(2):242, April 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039056`.

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[Ben69b] R. D. Benham. R69-1 transport time-delay simulation for transmission line representation. *IEEE Transactions on Computers*, C-18(1):83, January 1969. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1671128`.

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[Ber60a] R. H. Bergman. Tunnel diode logic circuits. *IRE Transactions on Electronic Computers*, EC-9(4): 430–438, December 1960. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219881`.

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[Ber60b] Ted W. Berwin. Frequency-to-period-to-analog computer for flowrate measurement. *IRE Transactions on Electronic Computers*, EC-9(1):62–71, March 1960. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5221606`.

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[Ber66a] A. J. Bernstein. Analysis of programs for parallel processing. *IEEE Transactions on Electronic Computers*, EC-15(5):757–763, October 1966. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038883`.

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[Bey63] W. A. Beyer. Uniqueness of weighted code representations, II. *IEEE Transactions on Electronic Computers*, EC-12(2):137, April 1963. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4037816`.

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[BFL+66b] N. A. Ball, H. Q. Foster, W. H. Long, I. E. Sutherland, and R. L. Wigington. A shared memory computer display system. *IEEE Transactions on Electronic Computers*, EC-15(5):750–756, October 1966. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038882.

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[BH67] G. D. Bergland and H. W. Hale. Digital real-time spectral analysis. *IEEE Transactions on Electronic Computers*, EC-16 (2):180–185, April 1967. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039026.

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[BHRS61] W. A. Barrett, F. B. Humphrey, J. A. Ruff, and H. L. Stadler. A card-changeable permanent-magnet-twistor memory of large capacity. *IRE Transactions on Electronic Computers*, EC-10(3): 451–461, September 1961. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219234.

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[Bic60] Theodore A. Bickart. Computation of an expression for the frequency spectrum of a two-sided signal. *IRE Transactions on Electronic Computers*, EC-9 (2):263–264, June 1960. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219831.

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[Bie68] B. C. Biega. R68-7 analog simulation of ferroresonant system including analysis of hysteresis loop. *IEEE Transactions on Computers*, C-17(2):192, February 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687317`.

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[Bin68] S. P. Bingulac. R68-38 accurate analog computer generation of Bessel functions for large ranges. *IEEE Transactions on Computers*, C-17(8):819, August 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687462`.

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[Bir67] L. G. Birta. Comment on "Nonobservable or Noncontrollable Analog Schemes for the Solution of a Special Class of Differential Equations". *IEEE Transactions on Electronic Computers*, EC-16(4):516–517, August 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039122`. See [Mar66b].

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[Bit59] Eric E. Bittmann. Thin-film memories. *IRE Transactions on Electronic Computers*, EC-8(2):92–97, June 1959. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219508`.

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[Cha60] S. C. Chao. Generalized RTL circuits — supplementary. *IRE Transactions on Electronic Computers*, EC-9(3):371–372, September 1960. CODEN IRELAO. ISSN 0367-9950. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219862.

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[Cha65b] Herbert Y. Chang. An algorithm for selecting an optimum set of diagnostic tests. *IEEE Transactions on Electronic Computers*, EC-14(5):706–711, October 1965. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038556`.

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[Cha68] Suresh Chander. On Dolotta–McCluskey technique. *IEEE Transactions on Computers*, C-17(1):95, January 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5008881`.

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[Che61b] Philip Warren Cheney. A digital correlator based on the residue number system. *IRE Transactions on Electronic Computers*, EC-10(1):63–70, March 1961. CODEN IRELAO. ISSN 0367-9950. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5219154`.

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[Che68a] Chi-Hau Chen. Computer processing of radiation sensor PCM data. *IEEE Transactions on Computers*, C-17(10):987–989, October 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687245`.

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[Che68b] K. A. Chen. Computer aided memory design using transmission line models. *IEEE Transactions on Computers*, C-17(7):640–648, July 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687420`.

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[Che68c] Tsai Hwa Chen. The use of delay lines in reading a Manchester code. *IEEE Transactions on Computers*, C-17(9):827–845, September 1968. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=1687470`.

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[Chi63] P. M. Chirlian. The fabrication of low resistances at liquid helium temperatures. *IEEE Transactions on Electronic Computers*, EC-12(2):148, April 1963. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4037820`.

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[Cho63a] Yohan Cho. A method of theoretical analysis of high-speed junction diode logic circuits. *IEEE Transactions on Electronic Computers*, EC-12(5):492–502, October 1963. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4037963.

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[Cho63c] C. K. Chow. Notices. *IEEE Transactions on Electronic Computers*, EC-12(6):963, December 1963. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038050.

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[Cho64] Yohan Cho. Author index. *IEEE Transactions on Electronic Computers*, EC-13(4):501, August 1964. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038223.

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[Cho65a] A. K. Choudhury. Annual cumulative author index. *IEEE Transactions on Electronic Computers*, EC-14(6):1000–1002, December 1965. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038634.

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[Cho65b] C. K. Chow. Statistical independence and threshold functions. *IEEE Transactions on Electronic Computers*, EC-14(1): 66–68, February 1965. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038354.

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[Cho66] A. K. Choudhury. Abstracts of current computer literature. *IEEE Transactions on Electronic Computers*, EC-15(6):963–975, December 1966. CODEN IEECA8. ISSN 0367-7508. URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038950.

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[Cho67] Woo F. Chow. Plated wire content-addressable memories with bit-

steering technique. *IEEE Transactions on Electronic Computers*, EC-16(5):642–652, October 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039155`.

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[Chu65a] Yaohan Chu. A destructive-readout associative memory. *IEEE Transactions on Electronic Computers*, EC-14(4):600–605, August 1965. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038512`.

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[Chu65b] Yaohan Chu. Direct execution of programs in floating code by address interpretation. *IEEE Transactions on Electronic Computers*, EC-14(3):417–422, June 1965. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038460`.

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[Chu66a] W. W. Chu. Computer simulation of waveform distortions in digital magnetic recordings. *IEEE Transactions on Electronic Computers*, EC-15(3):328–336, June 1966. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038769`.

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[Chu66b] W. W. Chu. Correction. *IEEE Transactions on Electronic Computers*, EC-15(3):327, June 1966. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4038768`.

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[Chu67a] Wesley W. Chu. A computer simulation of electrical loss and loading effect in magnetic recording. *IEEE Transactions on Electronic Computers*, EC-16(4):430–434, August 1967. CODEN IEECA8. ISSN 0367-7508. URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4039106`.

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[Chu67b] Wesley W. Chu. A mathematical model for diagnosing system failures. *IEEE Transactions on Electronic Computers*, EC-16

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